

09/940259

Inventor: FAZIO, MARIO












Status: 71 - RESPONSE TO NON-FINAL OFFICE ACTION ENTERED AND FORWARDED TO EXAMINER

Title: LOGIC AND MEMORY DEVICE INTEGRATION

Examiner: DINH, SON

GAU: 2824
Classification: 365/063.000

Incoming tab report (11 items, not sorted)

Img	Status	Doc Code	Document type	Receipt Date	Pages	Annotations
	7	ELC.	Response to Election / Restriction Filed	07/14/2005	2	
	13	FRPR	Foreign Priority Papers Filed	03/11/2003	57	Best Available Copy
	13	IDS	Information Disclosure Statement (IDS) Filed	02/04/2002	3	considered 10/16/05
	13	PA.	Power of Attorney (may include Associate POA)	12/18/2001	4	
	7	TRNA	Transmittal letter	08/27/2001	1	
	7	SPEC	Specification	08/27/2001	15	
	7	CLM	Claims	08/27/2001	15	
	7	ABST	Abstract	08/27/2001	1	
	7	DRW	Drawings	08/27/2001	5	
	7	OATH	Oath or Declaration filed	08/27/2001	3	
	7	136A	Authorization from Applicant to treat all replies as incorporating Extension of Time, and charge all fees to a deposit account	08/27/2001	1	